



Effetti di radiazione in celle di memoria non volatile di tipo Flash

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STMicroelectronics

Central R&D - Non Volatile Memory Technology Development
Agrate Brianza (Milano), Italy

Activity developed within a collaboration with



UNIVERSITA' DEGLI STUDI DI PADOVA
DEI, Department of Information Engineering



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Dr. Giorgio Cellere

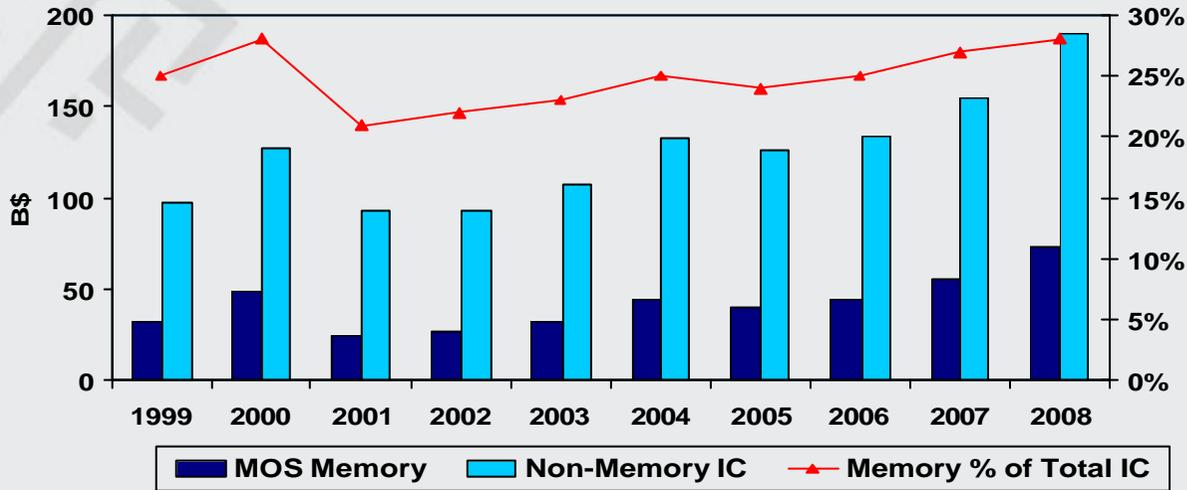


Outline

- ▣ **Semiconductor memory**
 - **Market**
 - **Flash applications**
- ▣ **The NOR Flash stacked gate memory cell**
 - Structure
 - Reading operation
 - Writing mechanism
 - Reliability basic
- ▣ **Radiation effects on Flash cell data retention**
 - TID: X and γ Rays
 - TID: Alpha particles
 - TID: Neutrons
 - SEE: Heavy ions
 - Technology Scaling effects
- ▣ **Summary**

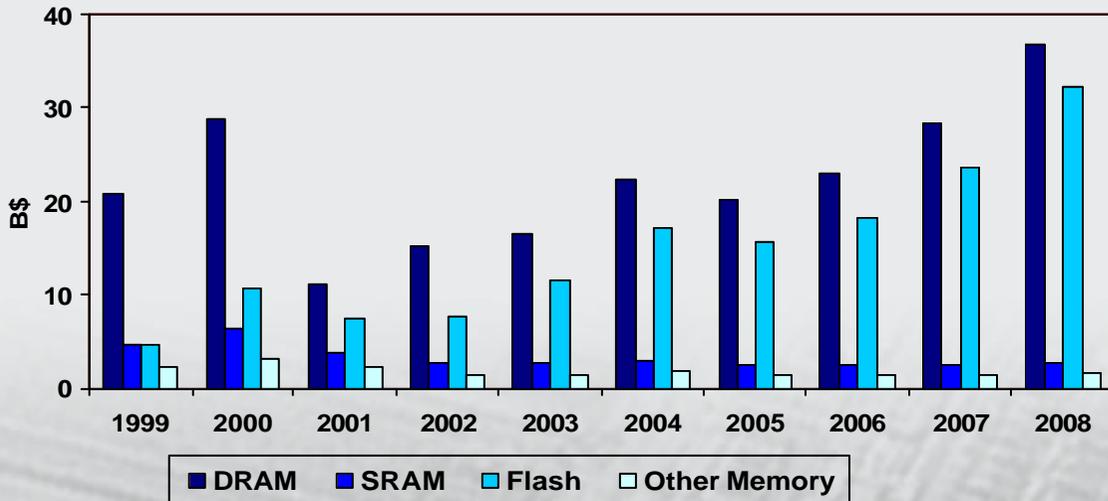
Memory Regaining Momentum

Memory Percent of Total IC Market



- ▣ Memories going to climb back to 30% of IC Market
 - DRAM \$ growth 17%
 - Flash \$ growth 23%

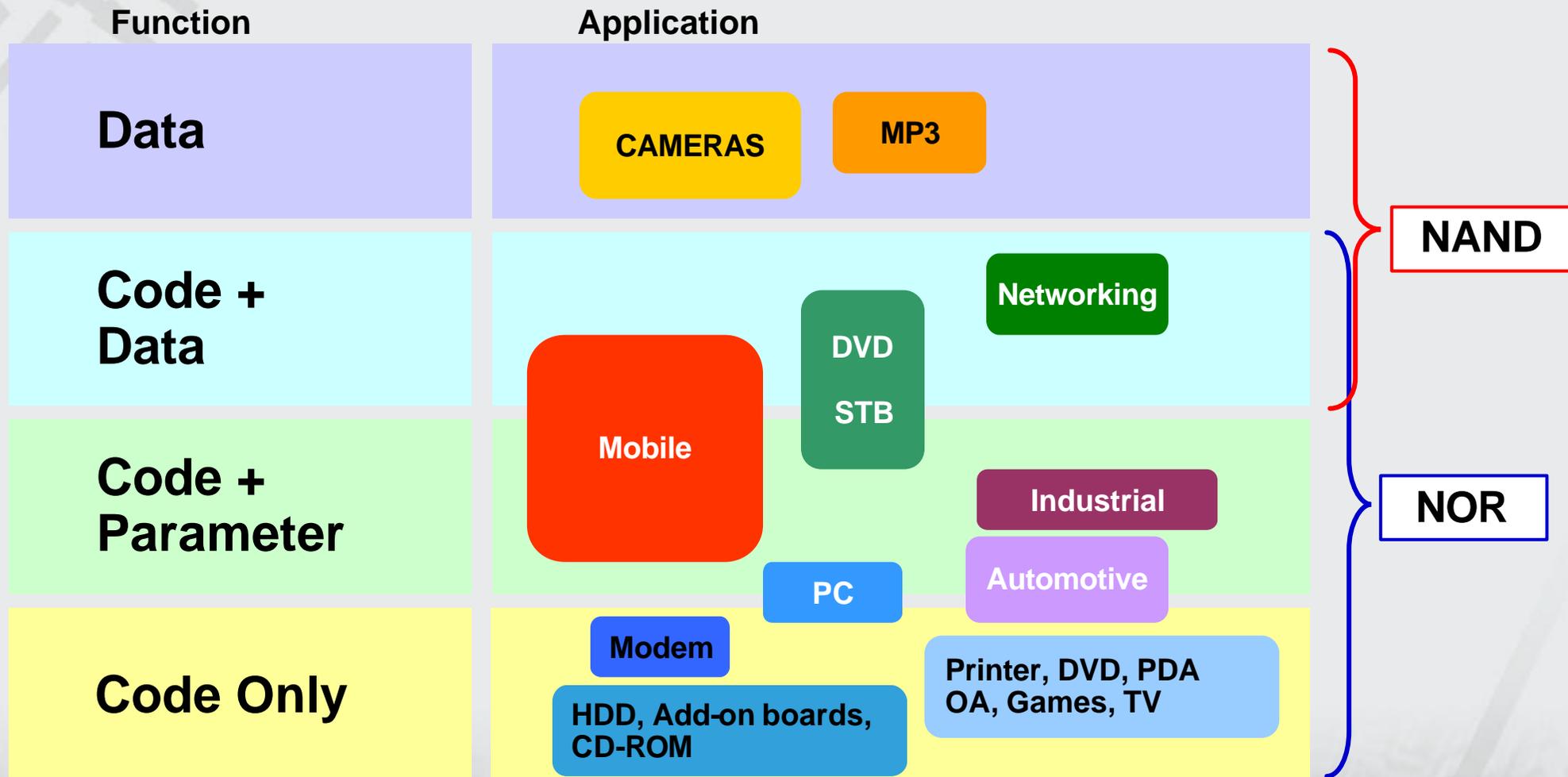
DRAM Dominates, Flash Gains



Source: WSTS, IC Insights



Flash Applications



Today in mass production

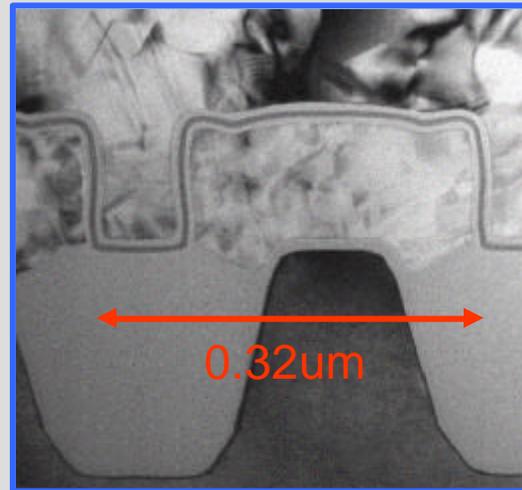
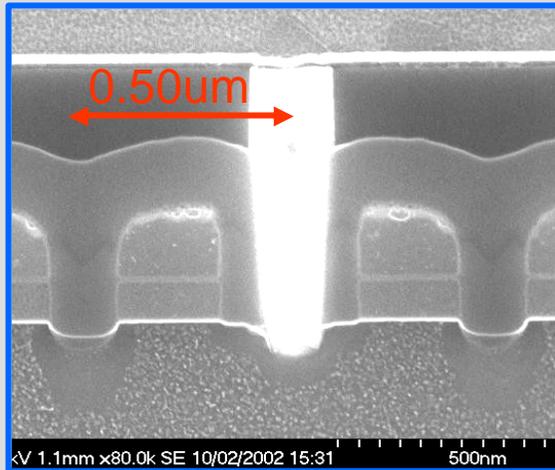
NOR: 128Mb, 130nm tech. NAND: 1Gb, 90nm tech



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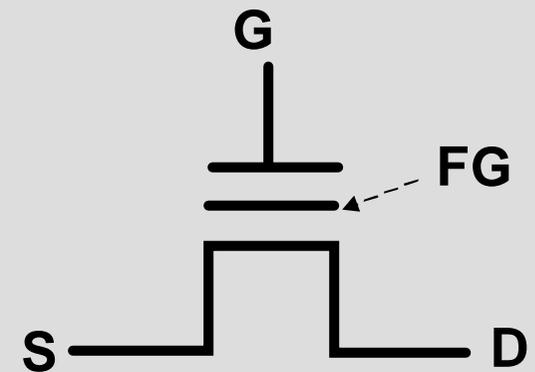
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NOR Stacked Gate Flash Cell

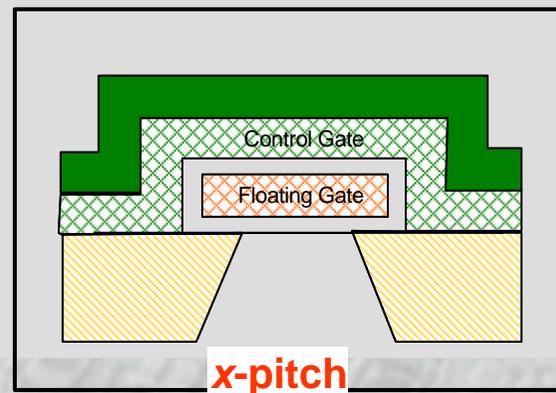
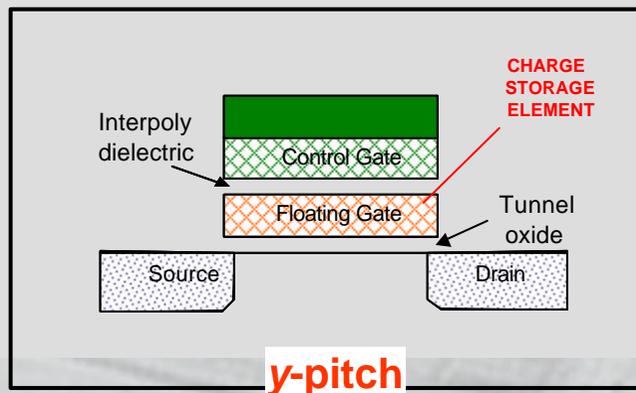


NOR cell

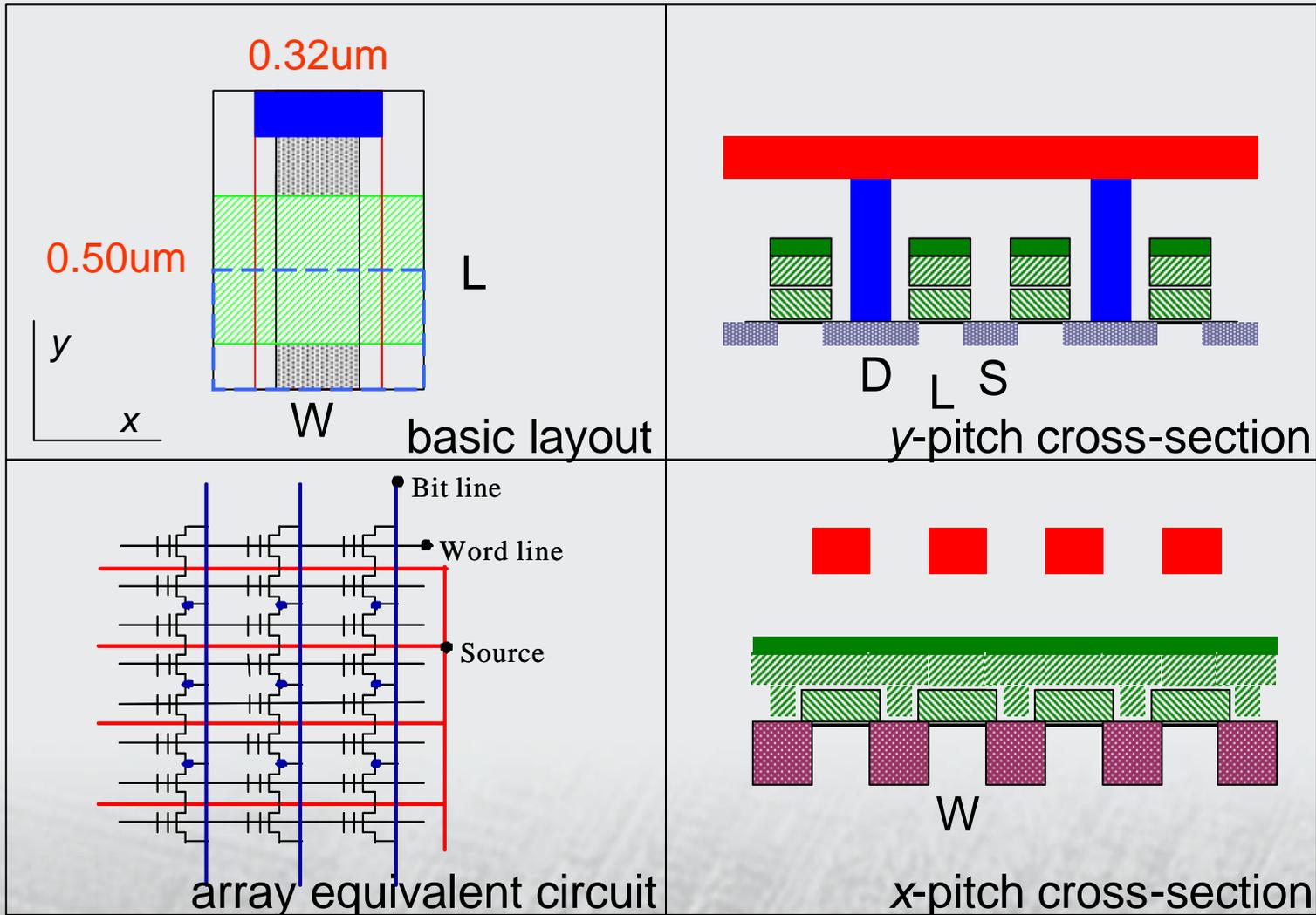
- Tunnel oxide th.: 9-10nm
- ONO EOT: 14-15nm
- Cell gate length: 200nm



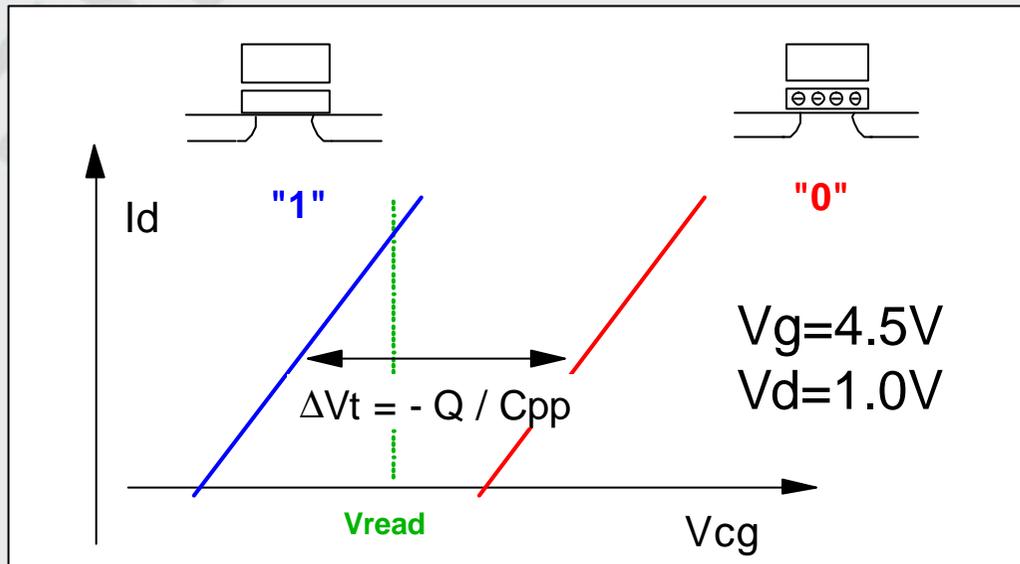
130nm Technology Node



NOR Flash Array

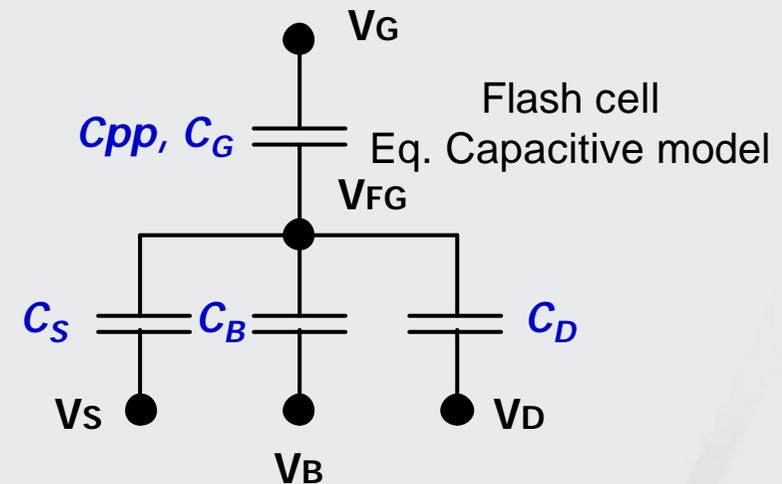


Reading Operation, Distributions and Working Range

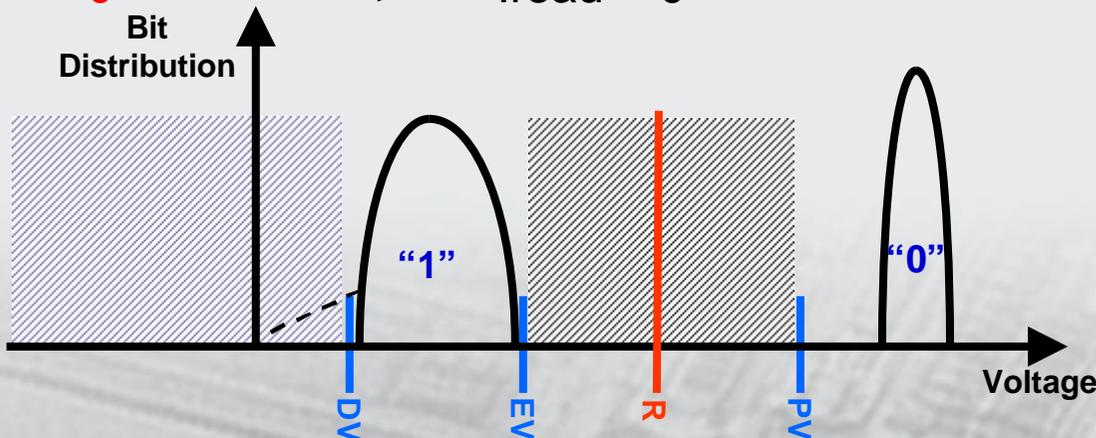


■ NOR Flash

- Read current: $I = 20-40\mu A$
- Random access: $t = 70-100ns$
- Serial throughput: $50MB/s$



"1" $\Rightarrow I_{read} > 0$
 "0" $\Rightarrow I_{read} = 0$



Working range (V): $V_t > 1.5$

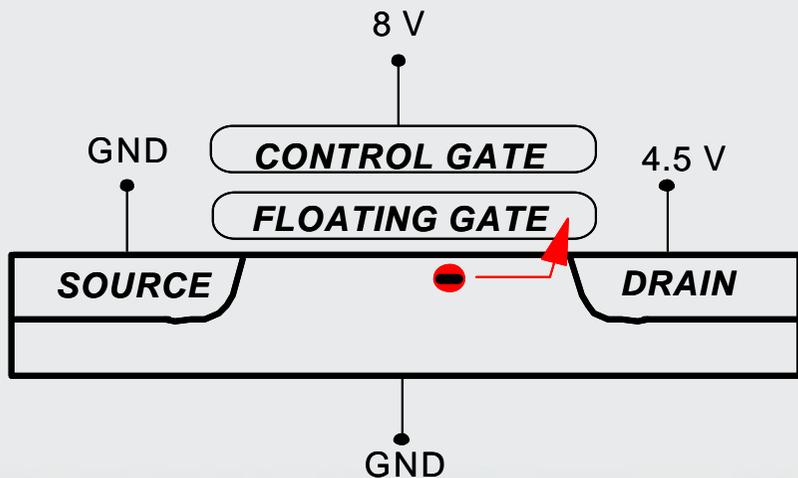
V_t shift (V): $DV_t > 3$

NOR Flash Writing Mechanism

- **Programming:**

channel hot electron (CHE) injection in the floating gate at the drain side

CHE program

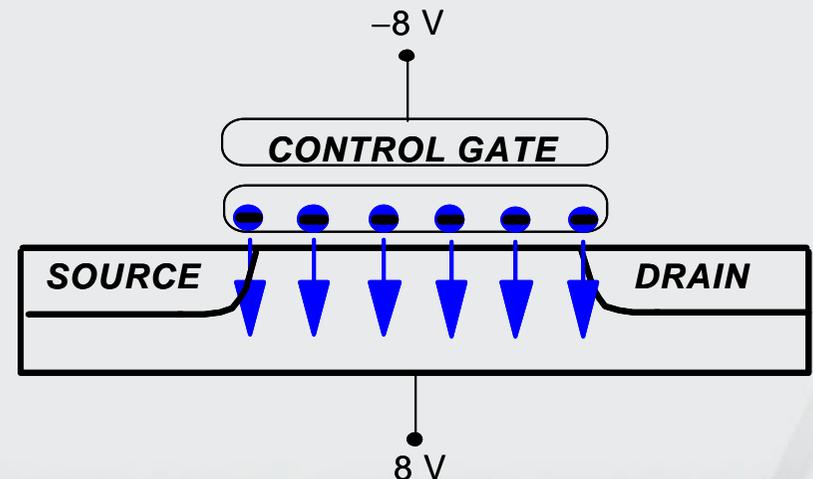


- Program is done at word level
- Program time $\sim 5\mu\text{s}$

- **Erasing:**

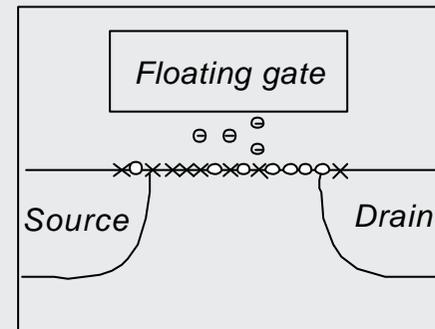
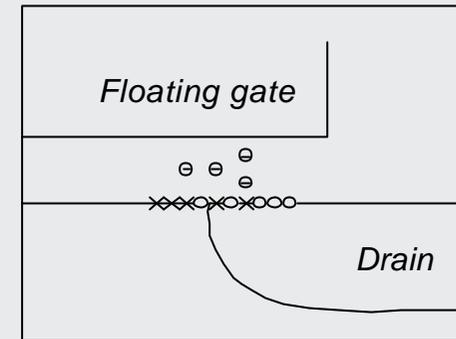
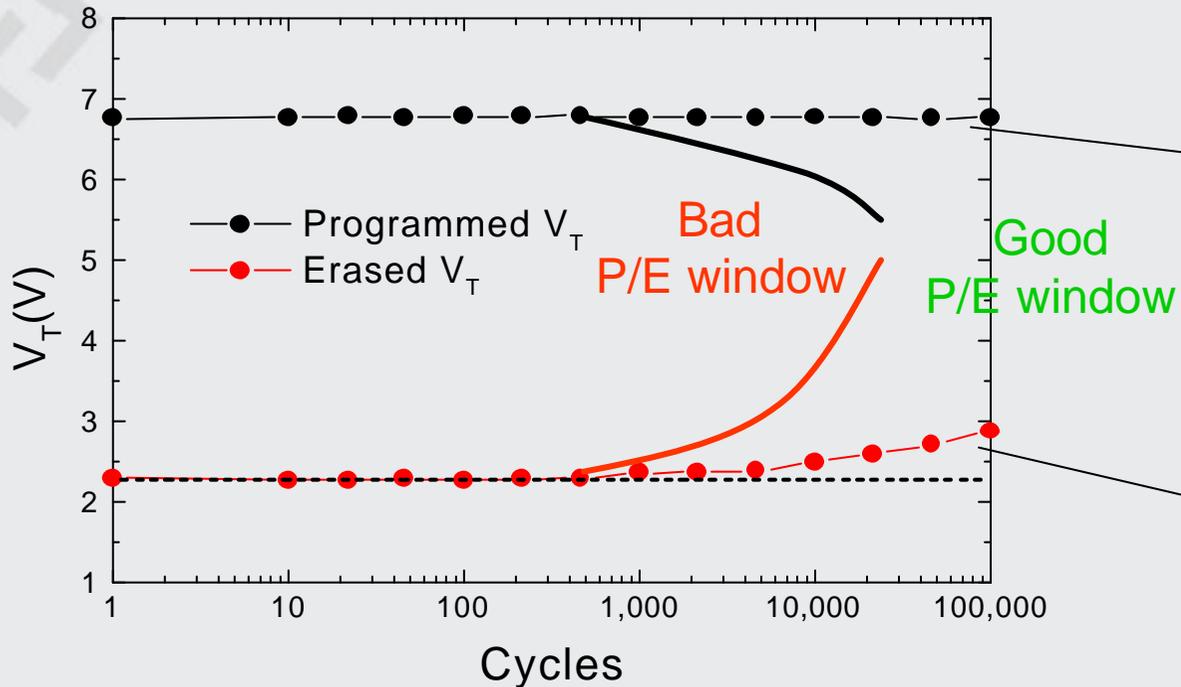
Fowler-Nordheim (FN) electron tunneling current through the tunnel oxide from the floating gate to the silicon surface

Channel FN erase



- Erase is done at sector level (0.5 Mb)
- Erase time $\sim 200\text{ms}$

Reliability basics: Flash Cell Endurance



- ❏ Interface states generation and charge trapping in the oxide occurs along the whole channel during Program/Erase
- ❏ Optimized cell design & operation are necessary to prevent Program/Erase window closure

Reliability basics: data retention

Flash Cell, 130nm Tech.

$$X=0.32\mu\text{m}$$

$$T_{\text{ox}}= 10\text{nm}$$

$$Y=0.50\mu\text{m}$$

$$T_{\text{ONO}}= 145\text{nm}$$

$$C_{\text{pp}} = 0.27 \text{ fF}$$

$$Q(0) = C_{\text{pp}} \Delta V_{\text{t}} = - 0.27 \text{ fF } 4\text{V} = - 1\text{fC} = \sim 6700 e^{-}$$

($1e^{-} \leftrightarrow 0.6\text{mV}$)

Let's suppose that a memory cell loses its data if the threshold falls below 1V

$$Q_{\text{leak}} = - 0.27 \text{ fQ} = \sim 1600 e^{-}$$

The memory cell has to guarantee 10 years of data retention, then:

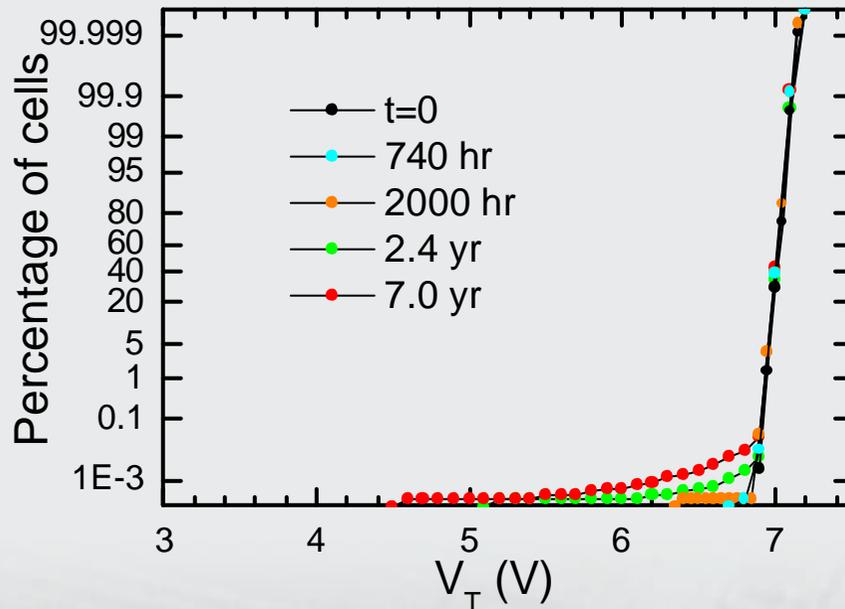
$$I_{\text{leak}} < 10^{-24} \text{ A} \sim 3 e^{-}/\text{week}$$



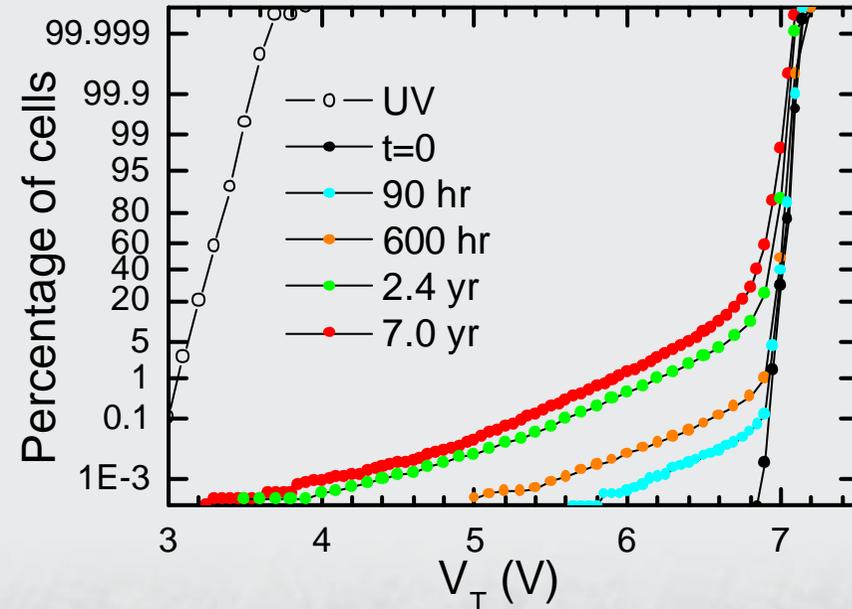
Effect of Cycling on Data Retention

Data retention tests at room temperature – $T_{ox}=8\text{nm}$
Cell V_T distribution for 1Mb arrays

10 cycles

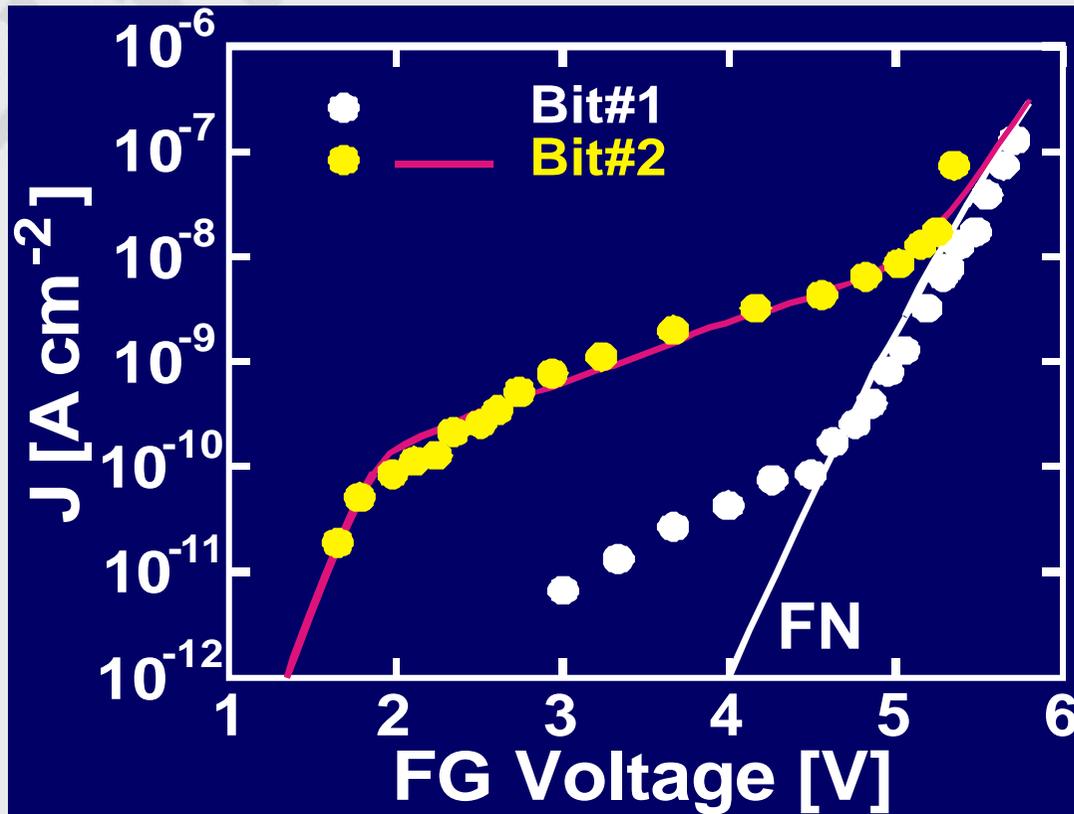


10^5 cycles

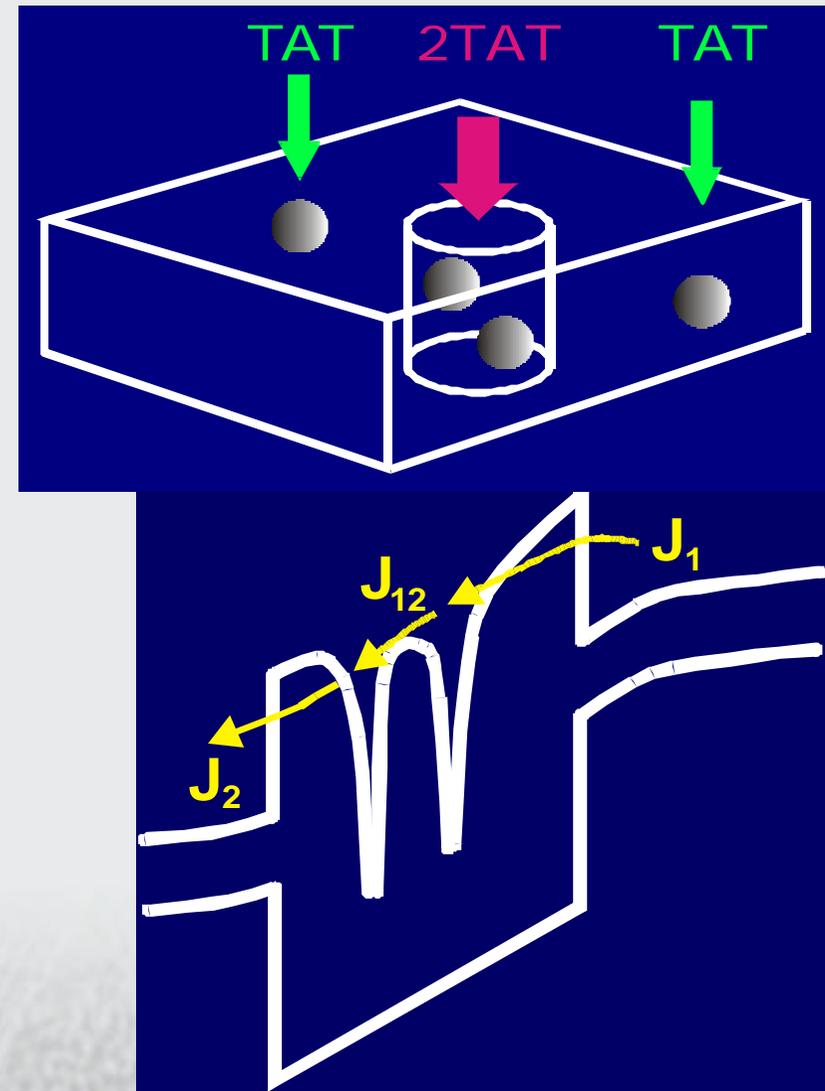


SILC: Stress Induced Leakage Current

Physical Model: Trap Assisted Tunneling



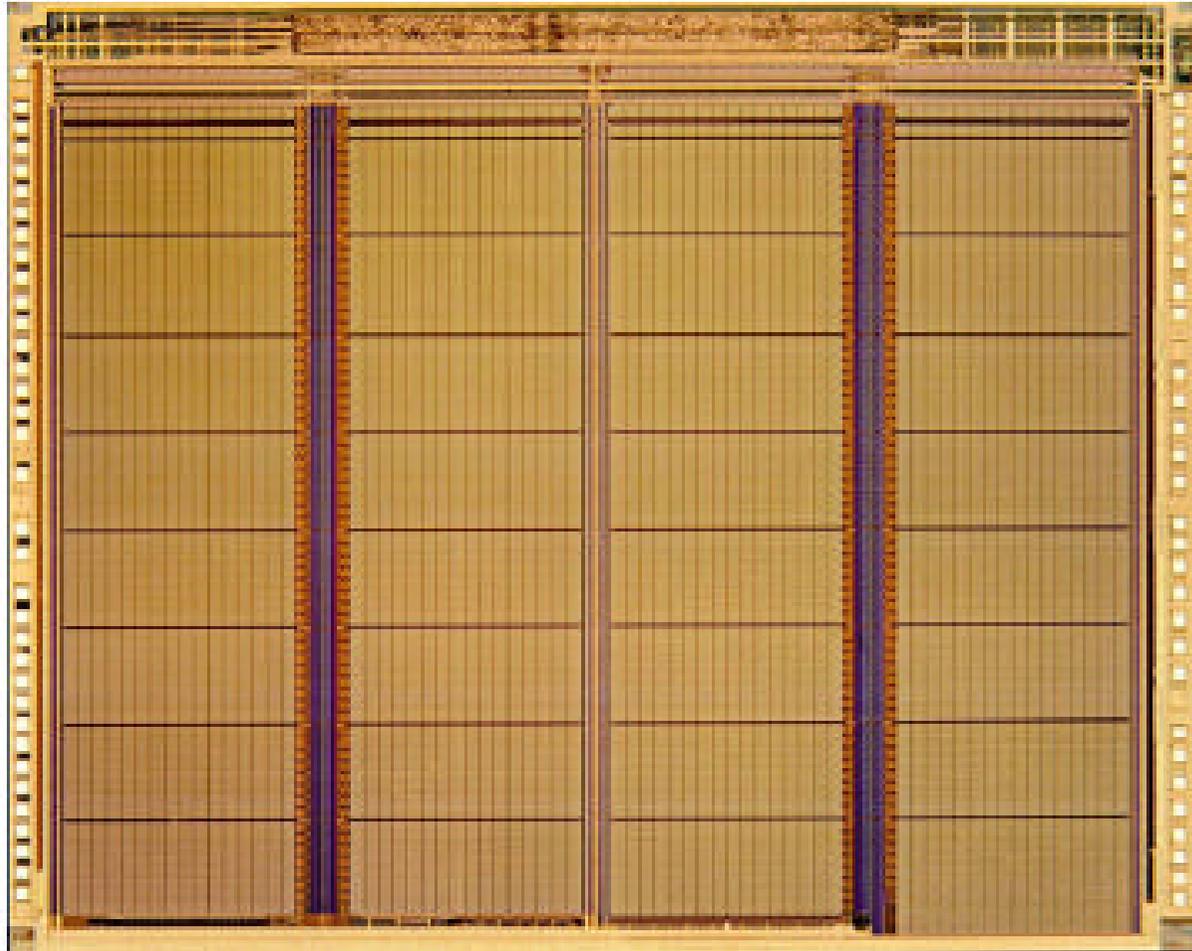
Typ SILC current $\sim 10^{-20}$ A
 \Rightarrow Used $T_{ox} > 10\text{nm}$



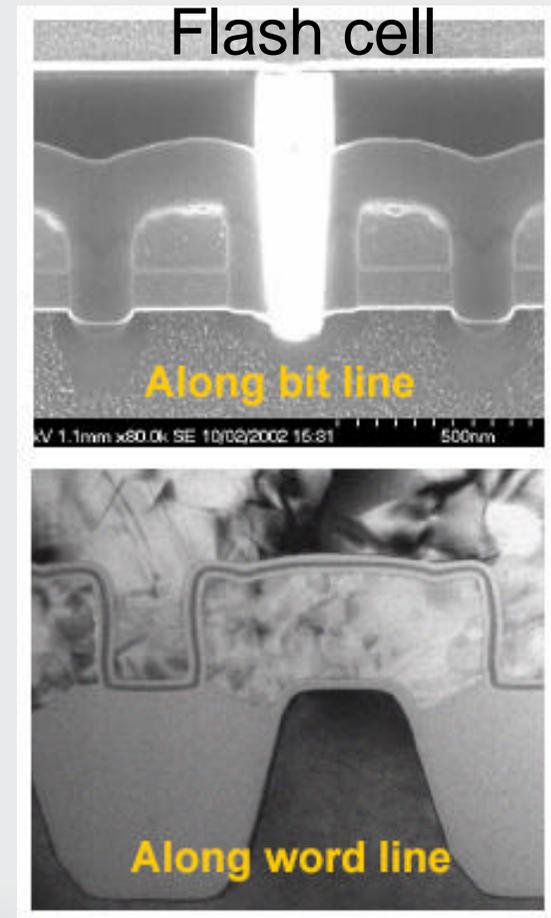
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CHIP OVERVIEW, NOR 130nm technology



128 Mb, 1.8V, Multibank



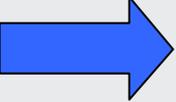
Chip size 36mm²

Array efficiency >60%

TID(1): X and γ rays experiments results

NOR FLASH

- Tests on 0.13-0.18 μ m tech, 4-32Mbit parts, 1.8-3.0V Vdd, by using the X- γ rays facility at INFN Laboratori Nazionali di Legnaro, Pd, I.

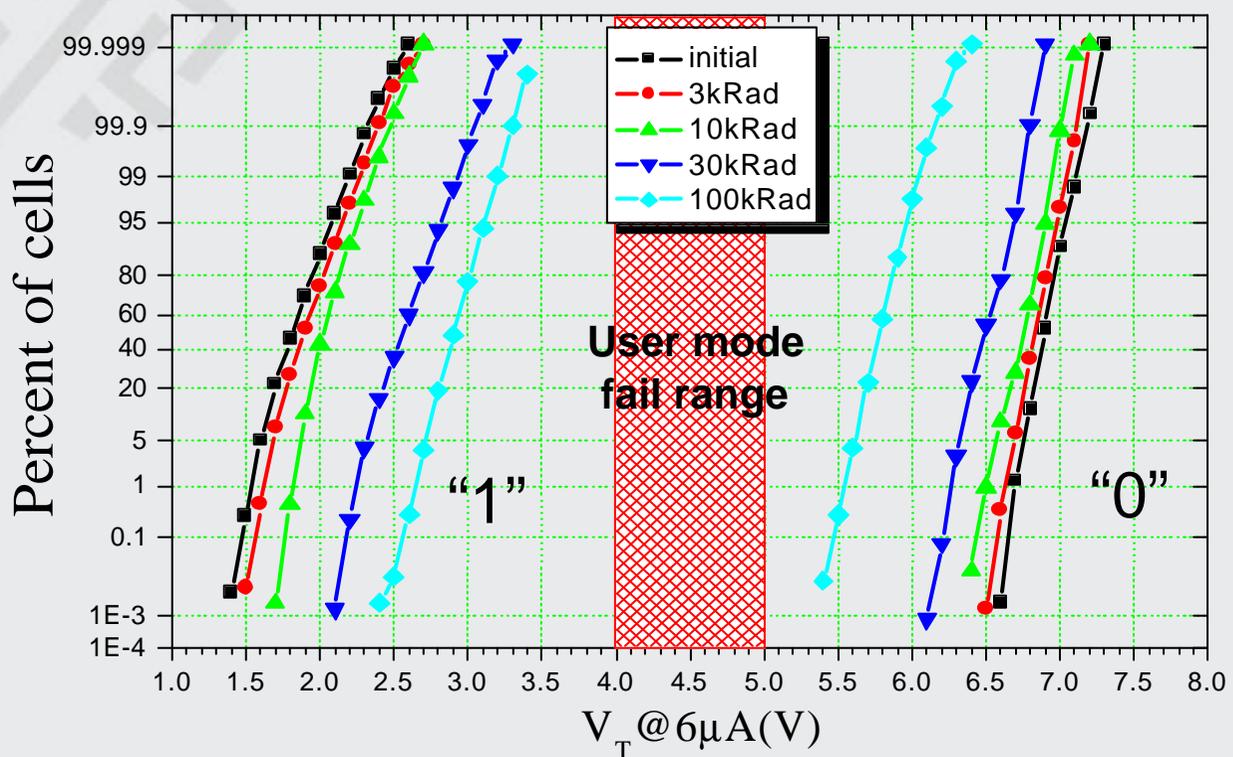


Full-spec parts after at least 100kRad*, unpowered
Full-spec parts after at least 10kRad*, biased

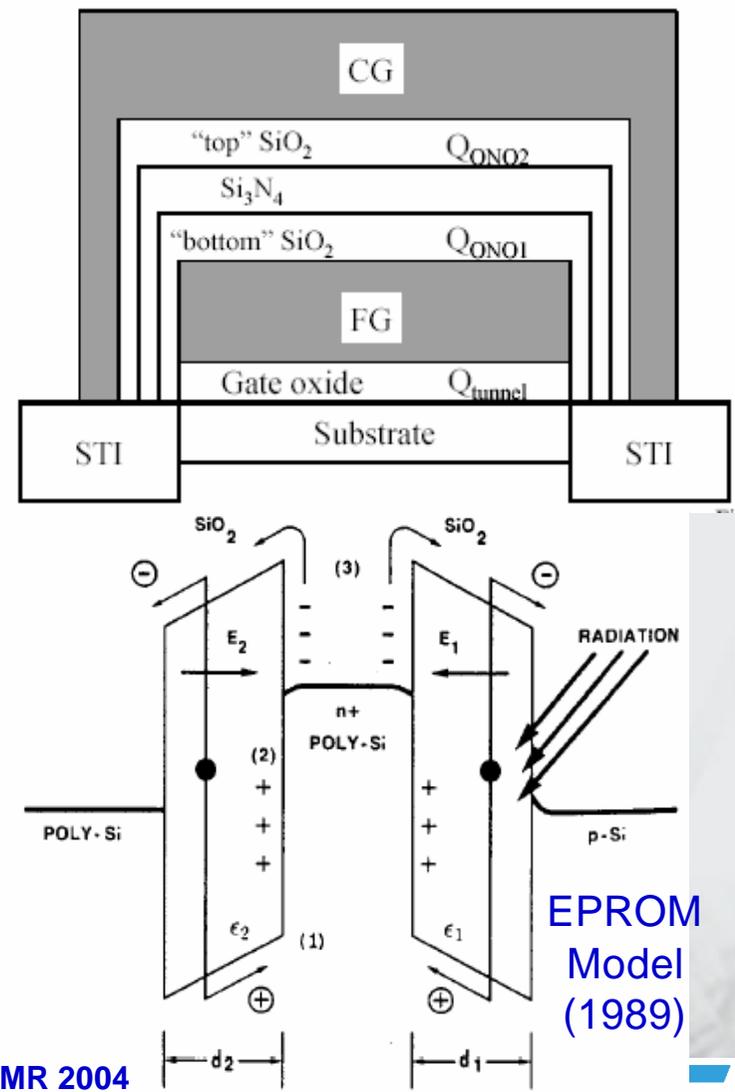
- As reference:
 - aeroportual X-ray inspection dose: ~5-50mRad
 - PCB X-ray inspection dose: <0.1kRad

* (10keV X-rays or ^{60}Co γ -rays, Si dose)

Flash cell V_t distribution before/after X-rays exposure



Data from 0.5Mcell, diff. samples, 0.15µm tech



EPROM Model (1989)

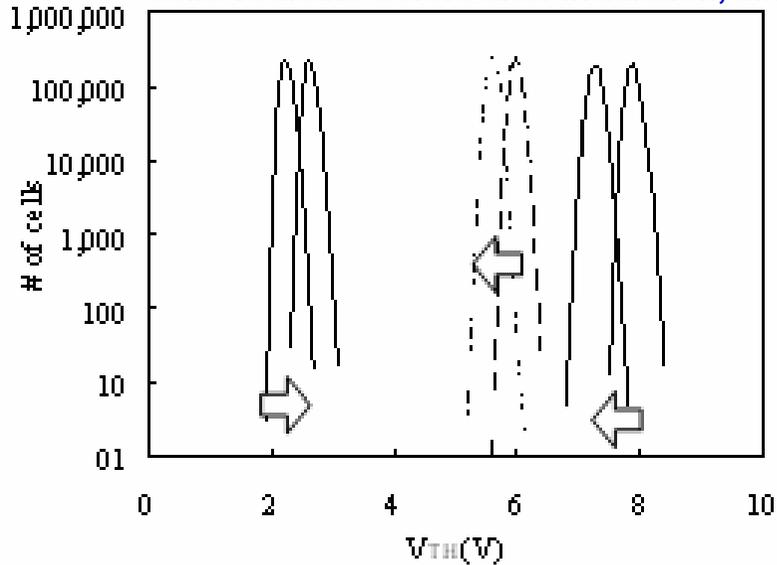
Source: 10keV x-rays. S_i dose

Cellere et al., TDMR 2004



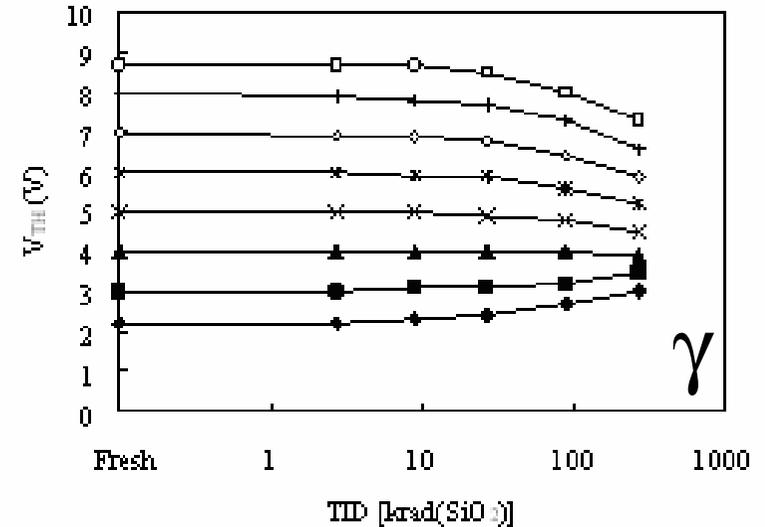
X and g rays effects

Cells Vt distrib before/after irradiation, diff. Eox



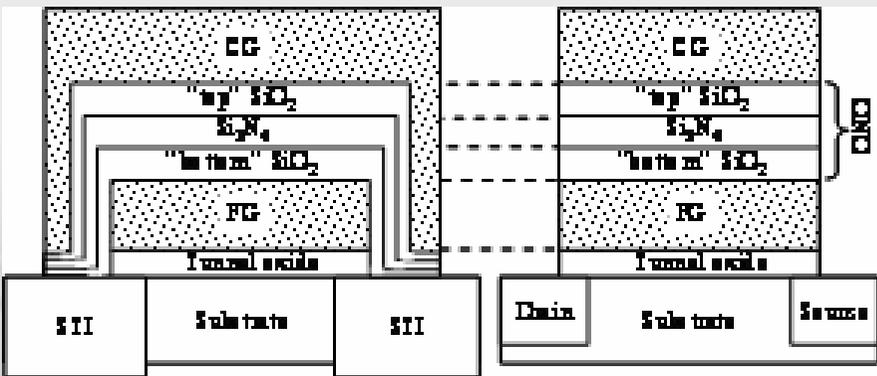
^{60}Co γ -rays

Cells Vt shift vs dose, diff. Eox

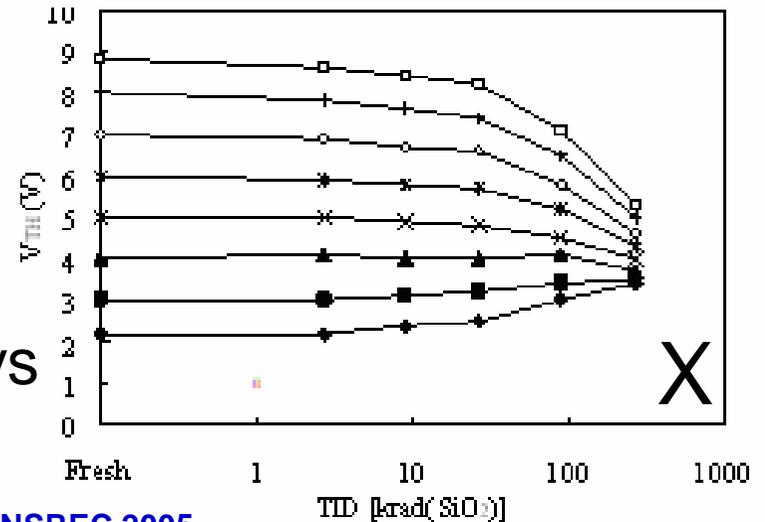


γ

Test-chip NOR 0.13um tech.
Tox 10nm, Tono 15nm



10keV X-rays



X

Cellere et al., sub. NSREC 2005

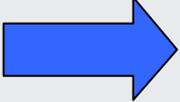


TID(2): Alpha particle experiments results

NOR FLASH

- Accelerated tests performed on 0.15um tech, 32Mbit, 1.8V, unpowered parts by using the ^{241}Am alpha source in ST Crolles, F. (JEDEC standard JESD89)

The alpha particles effects are negligible.



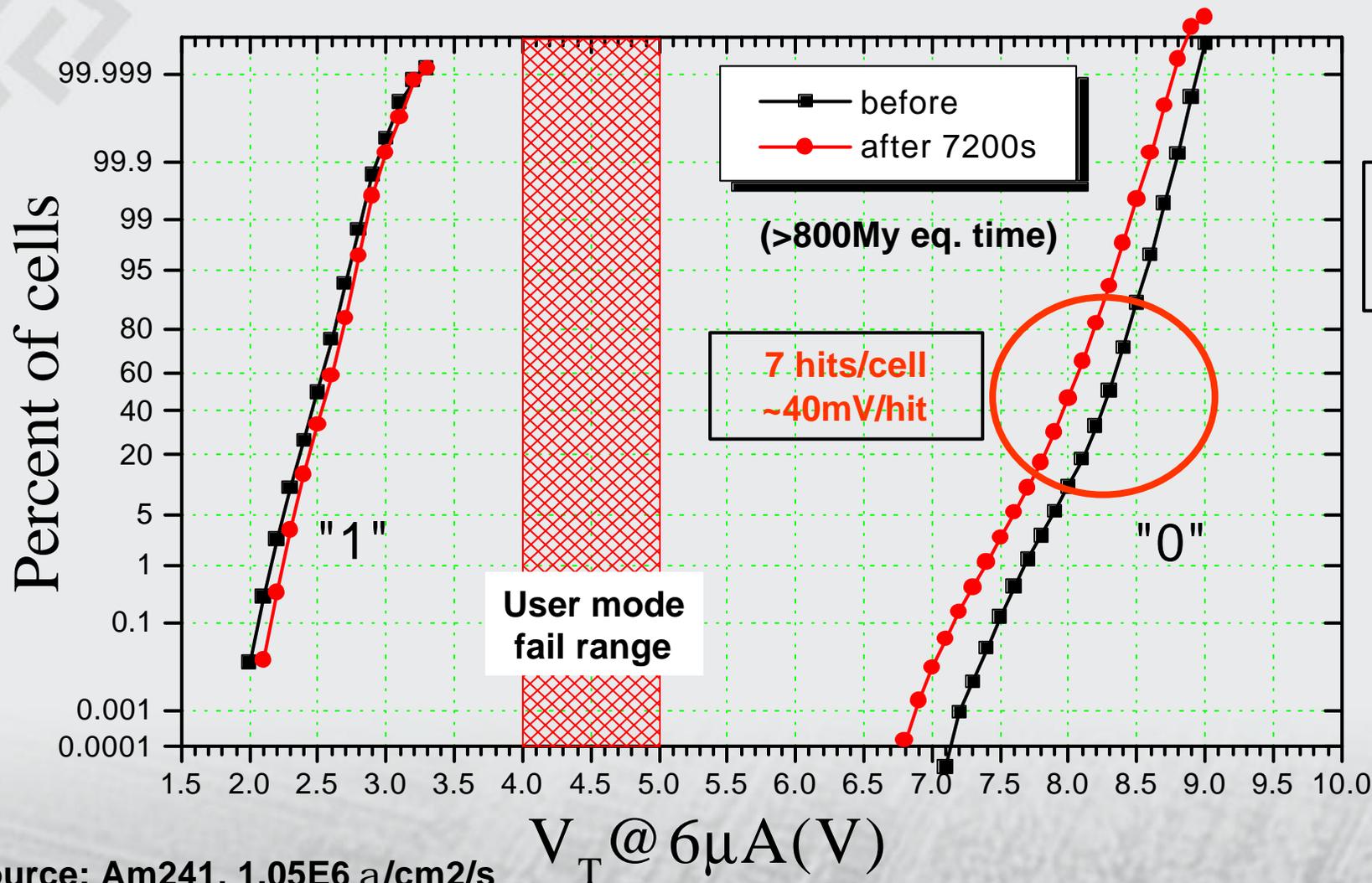
No fails in read pattern after **more than 800 million years** of equivalent time in std environment*

- Expected array failure rate*: $< 10\text{E-}6$ FIT/Mbit, unpowered

*with total flux from the package and process materials equal to 0.001 alpha/cm²/h

□ 1 FIT= 1 Failure In Time= 1 fail/10+9 hour

Flash cell V_T distribution before/after alpha particle exposure



TID(3): neutron experiments results

NOR FLASH

- Accelerated tests performed on 0.13-0.18um tech, 4-32Mbit, 1.8-3.0V, unpowered parts by using the Los Alamos National Labs. neutron source (JEDEC standard JESD89)

The neutrons effects are negligible.

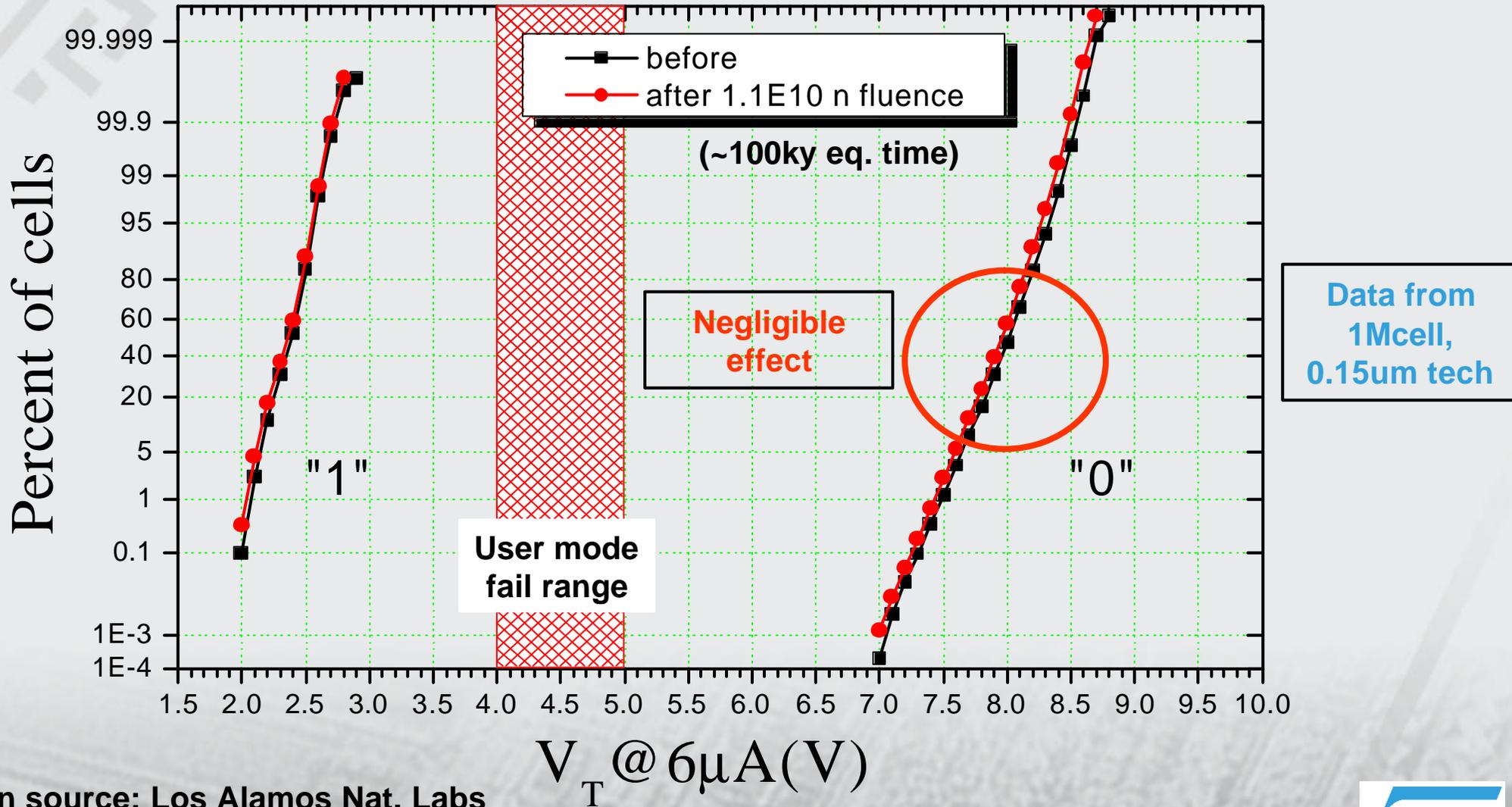
➔ No fails in read pattern after **more than 100.000 years** of equivalent time at ground level*, unpowered

- Expected array failure rate*: $\ll 0.04$ FIT/Mbit, unpowered

*with total flux at ground level equal to 14 n/cm²/h (NY sea level)

□ 1 FIT= 1 Failure In Time= 1 fail/10⁺⁹ hour

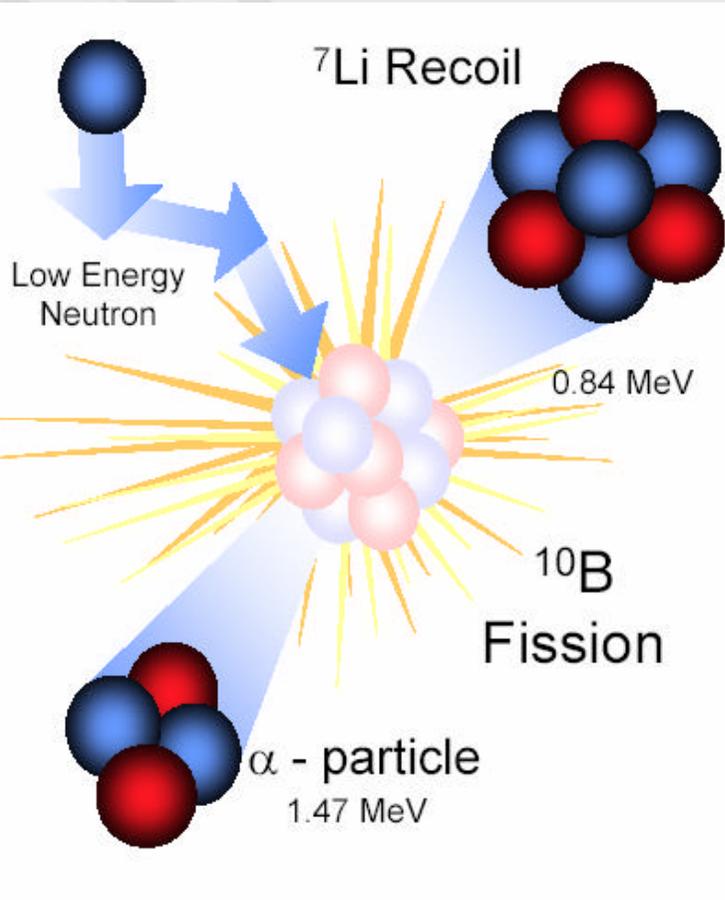
Flash cell V_T distribution before/after neutron exposure



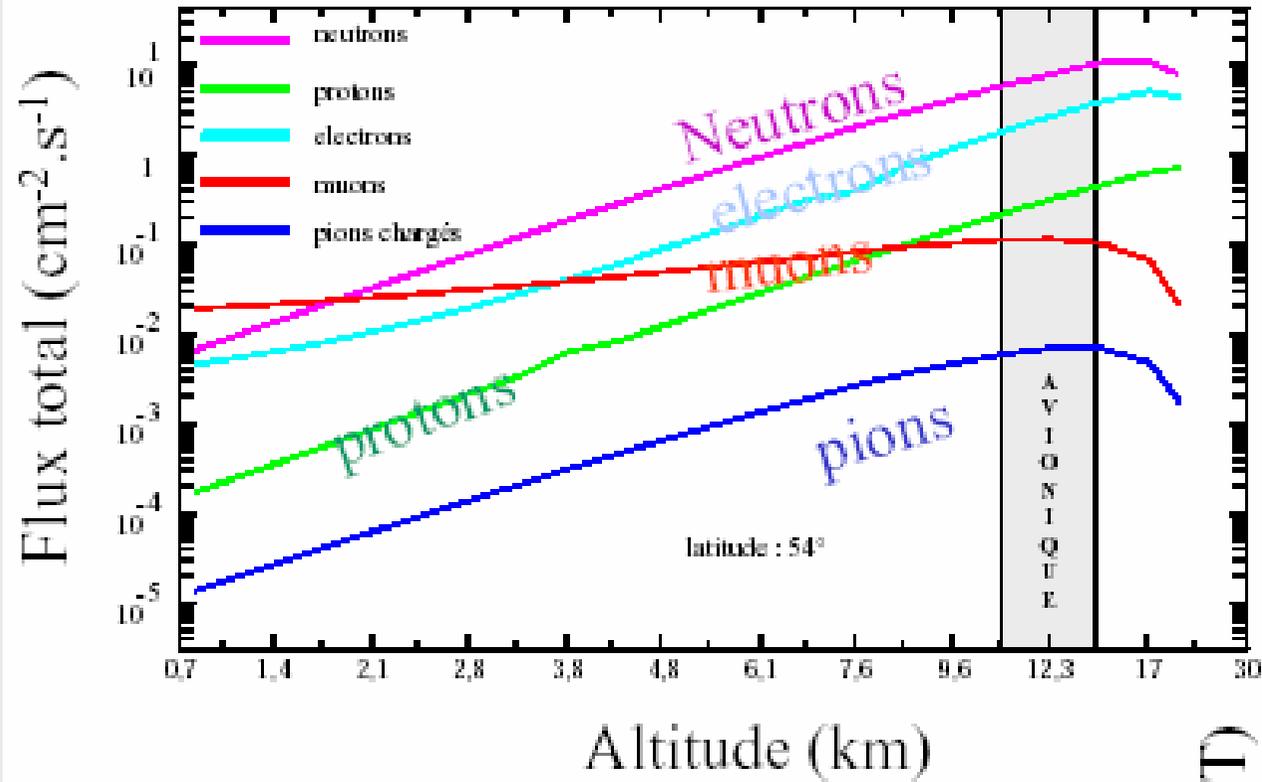
n source: Los Alamos Nat. Labs



More about neutrons



Main neutrons effect:
 ${}^{10}\text{B}$ fission
 => Again, alpha case



The neutron flux increases by 3 decade from 0 to 12km (avionic quota). Also at avionic altitude the neutrons effects are still expected to be negligible (previous data became 100 year equivalent)

SEE: heavy ions experiments results

NOR FLASH

- Tests performed on 0.09-0.18um tech, 4-32Mbit, 1.8-3.0V, unpowered parts by using the SIRAD irradiation facility at LNL-INFN Laboratori Nazionali di Legnaro, Pd, I.

The heavy ions effects **are not** negligible.

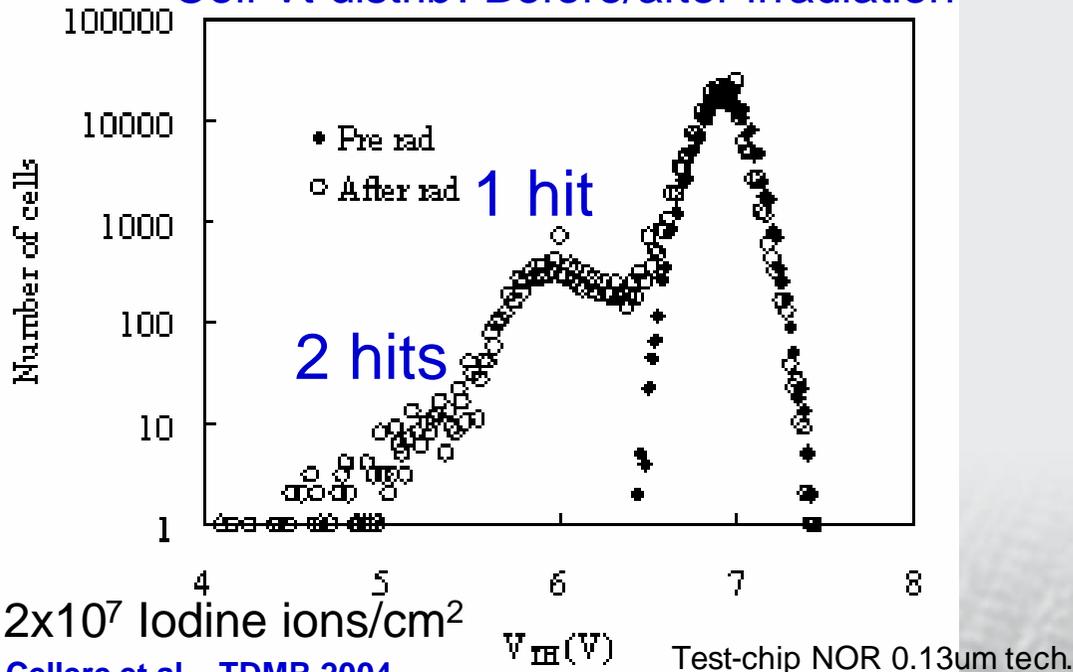
 A fail in read pattern may appears just after a single, high LET, hit!

However, the flux of high LET heavy ions at ground level **is** negligible, thus this is an issue only for aero-spatial applications

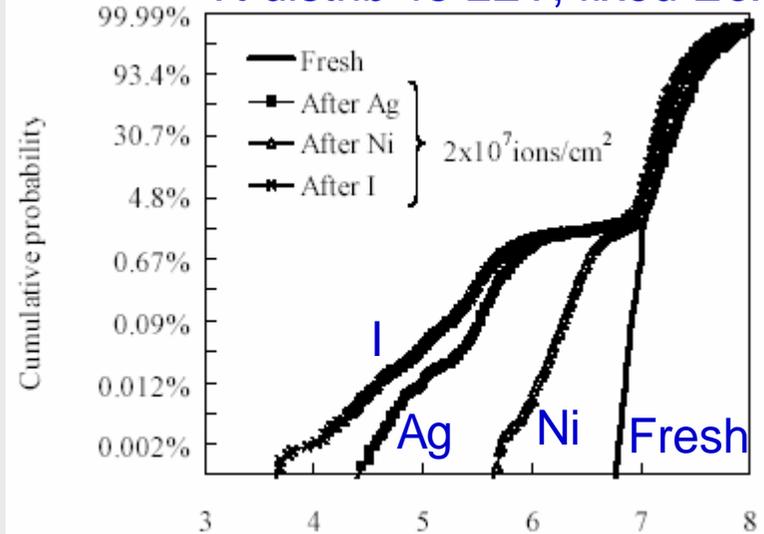
Heavy Ions effects (1)

Ion	E (MeV)	LET in SiO ₂ (MeVcm ² /mg)
C	87.15	1.68
F	128.8	2.29
Cl	177.8	13.23
Ti	203.2	20.93
Ni	210	28.14
Br	250.3	40.88
Ag	266.2	57.34
I	286.6	64.15

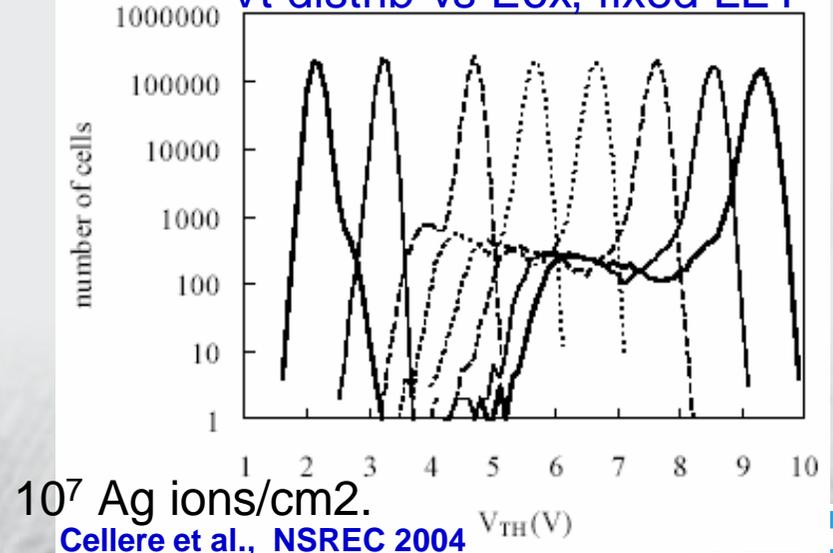
Cell Vt distrib. Before/after irradiation



Vt distrib vs LET, fixed Eox

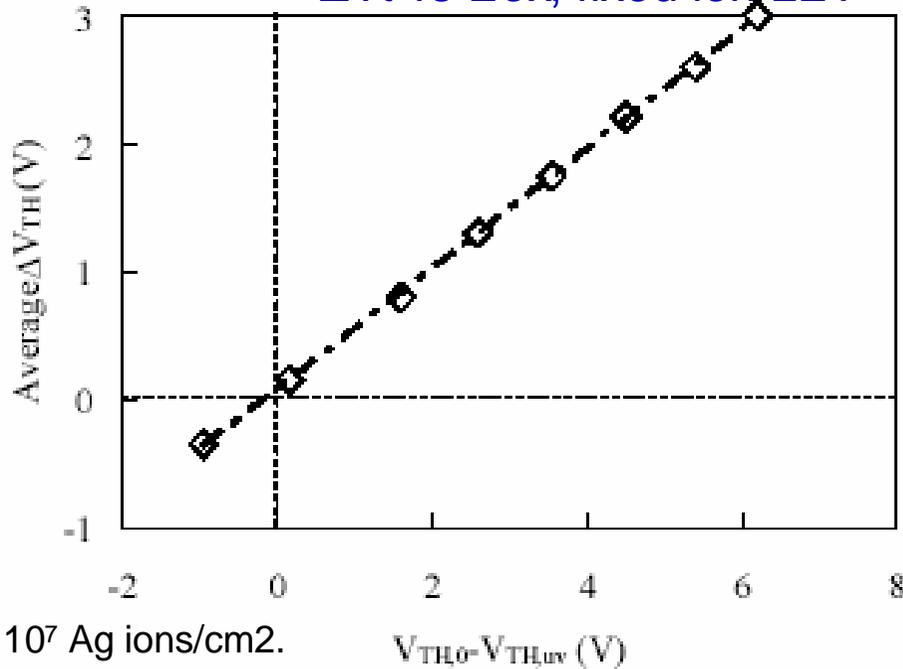


Vt distrib vs Eox, fixed LET

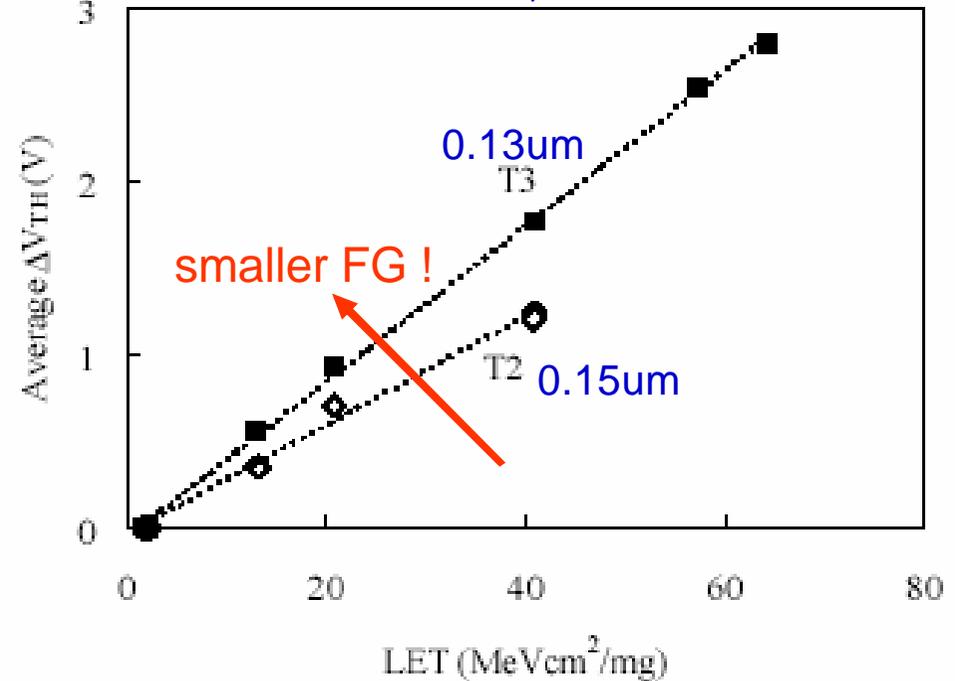


Heavy Ions effects (2)

ΔV_t vs E_{ox} , fixed ion LET

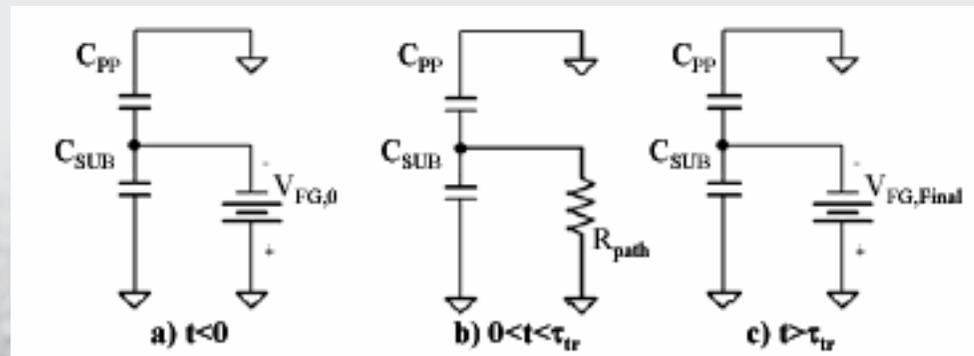


ΔV_t vs LET, fixed E_{ox}



The proposed model:
the impinging ion set-on a
temporary conduction path
through the tunnel oxide

Cellere et al., NSREC 2004



Heavy ions post-irradiation effects (1)

Room temperature retention trial on heavy ions irradiated samples

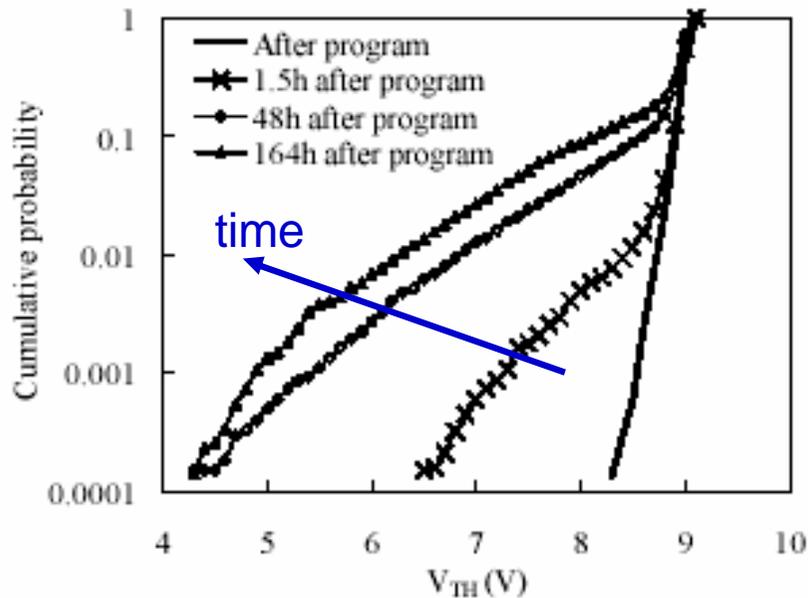


Fig.1. Cumulative distribution of V_{TH} for cells hit by Iodine ions, after being re-programmed.

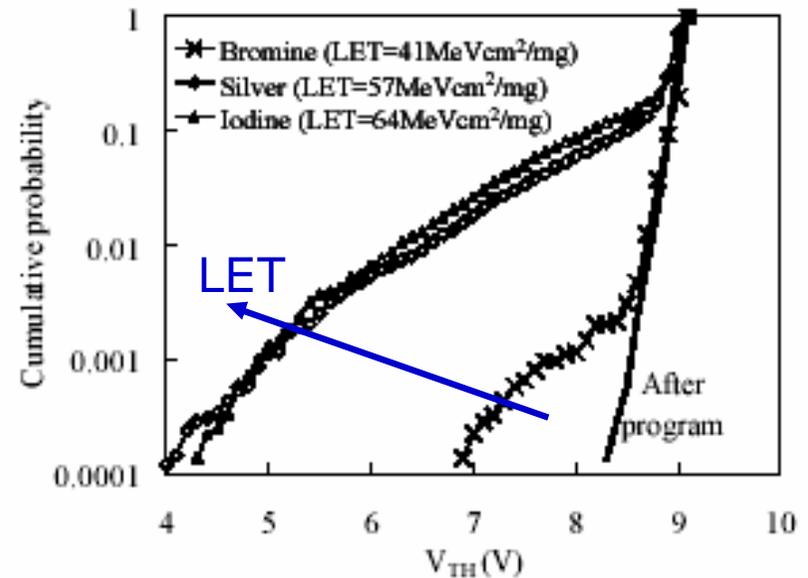


Fig.2. Cumulative probability of V_{TH} for cells being hit by different ions, immediately after program, and 164 hours after program. $E_{OX}=3MV/cm$ during irradiation.

RILC: Radiation Induced Leakage Current

Heavy ions post-irradiation effects (2)

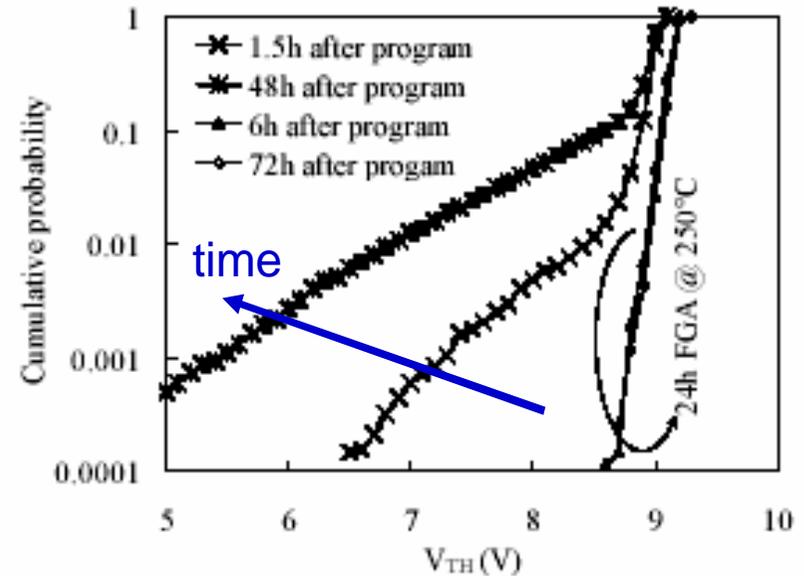
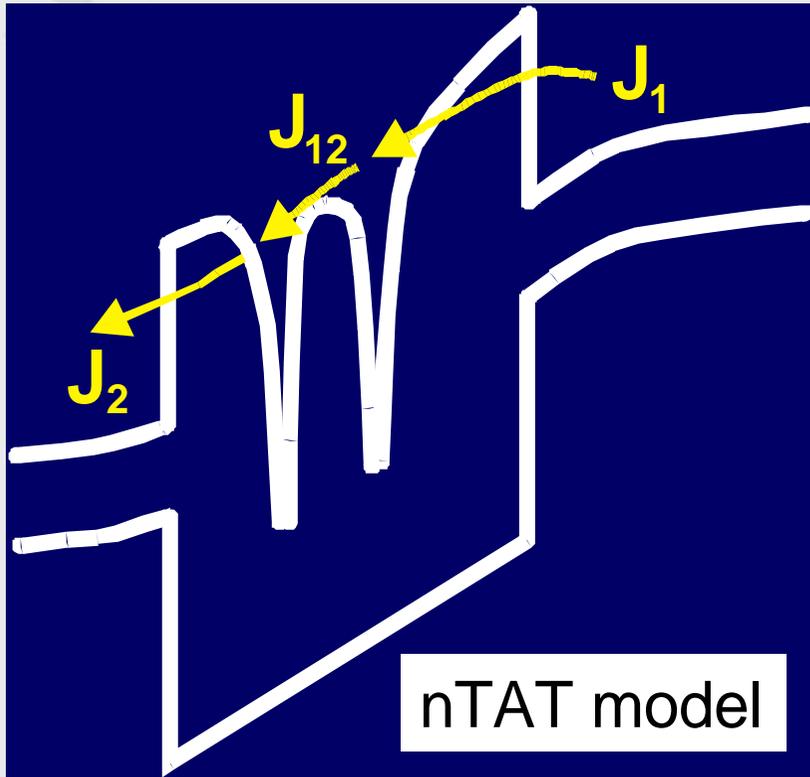


Fig.4. Cumulative probability of V_{TH} for two devices: the first was programmed after being irradiated, the second was at first subjected to Forming Gas Anneal, then programmed.

Cellere et al., sub. NSREC 2005

RILC from Trap Assisted Tunneling

Technology scaling effects

As technology scales down,
Tox thickness \sim const (or thinner)
Tono thickness \sim const (or thinner)
 \Rightarrow Interactions \sim const (or lower)

At fixed radiation fluency,
#event/cell \sim cell_area
Impact effect, $\Delta V_t \sim 1/C_{pp}$
 \Rightarrow #event * $\Delta V_t \sim$ area/Cpp

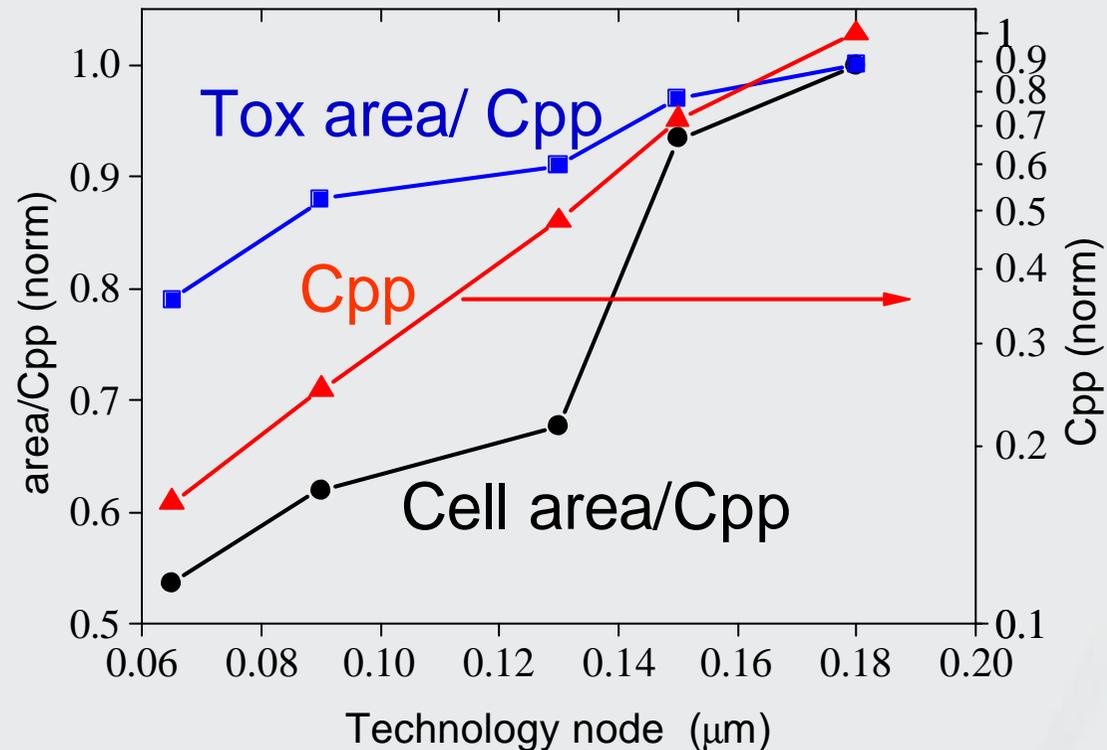
TID \sim area/Cpp

SEE $\sim 1/C_{pp}$

At fixed radiation fluency,

TID phenomena (X, γ , α , n) don't worsen with technology scaling

SEE phenomena from high LET heavy ions worsen with technology scaling



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Summary

- The current semiconductor NVM mainstream is based on the Flash technology and it is expected that NOR and NAND Flash will dominate NVM production for the rest of this decade.
- Accelerated tests has been performed on 0.09-0.18um tech, 4-32Mbit, 1.8-3.0V parts by using the Los Alamos National Labs. neutron source, USA, a ^{241}Am alpha source in ST Crolles, F, (JEDEC standard JESD89), the X- γ rays and the SIRAD irradiation facility at LNL-INFN Laboratori Nazionali di Legnaro, Pd, I.
- Focusing on Flash cell data retention, it is measured an absolutely negligible effect of alpha and neutron radiations, while in X and γ rays case the effects are still negligible after at least a 100kRad dose.
- Flash memory is extremely robust against irradiations. While Flash memory does possess some sensitivity to cosmic irradiation, other circuits are many orders of magnitude more vulnerable, thus Flash will not be the reliability-limiting factor for a typical system.



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